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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Mitsushi FUJIKI

Group Art Unit: 2823

Application Number: 10/772,253

Examiner: Thanh V. Pham

Filed: February 6, 2004

Confirmation Number: 6491

For:

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE INCLUDING A LOW-TEMPERATURE LOWER ELECTRODE-

FORMING STEP

Attorney Docket Number:

042068

Customer Number:

38834

<u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 C.F.R. §1.97(c)(1)/(e)(1)</u>

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

October 10, 2007

Sir:

Applicants direct the attention of the Patent and Trademark Office to the documents listed on the attached Form PTO/SB/08. A copy of each non - U.S. document is attached.

This Information Disclosure Statement (IDS) is being submitted after issuance of a first official action on the merits and expiration of the three month period following the filing date or the entry of the national stage for the above-captioned application, but prior to issuance of either a final official action or a Notice of Allowance.

The undersigned hereby certifies that each item of information contained in the IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the IDS. A copy of the foreign communication citing the documents is attached.

Information Disclosure Statement under 1.97(c)(1)

Attorney Docket No. 042068

Application No. 10/772,253

Each listed document was first cited in a communication from a foreign patent office and

the communication was not received by any person defined in 37 C.F.R. §1.56(c) more than 30

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Respectfully submitted,

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Enclosures: P7

PTO/SB/08

5 Documents

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